

Remarks

In the non-final Office Action dated January 30, 2008, the drawings stand objected to; claims 1-32 stand rejected under 35 U.S.C. §§ 112(1) and 112(2); and claims 7 and 23 stand further rejected under 35 U.S.C. § 112(2).

There are no prior art rejections of record. The amendments to the drawings are believed to overcome the objections thereto, and the following discussion and related amendments to the claims address the Section 112 rejections. For reasons as further explained below, Applicant believes that the claims are in condition for allowance.

FIG. 1 has been amended as suggested by the Examiner. FIG. 2 has been amended to emphasize the connection of the instruction (at 10) and the coefficient (at 16) to the ALU 3 and multiplexers 5, 15 and 21. While FIG. 2 showed this connectivity prior to amendment, the replacement drawing sheet filed herewith emphasizes this connectivity by showing two connection points/nodes at connector intersections near the ALU 3.

Applicant believes that these changes overcome the objections to the drawings.

Applicant respectfully traverses the Section 112 rejections. The example embodiments discussed in the specification and shown in FIG. 2 (prior to amendment) clearly describe and show circuit components connected and operated in a manner that supports all of the claim limitations in a manner consistent with Sections 112(1) and 112(2). Referring to FIG. 2, the instruction 10 is shown as coupled to the ALU 3, the multiplexers 5, 15 and 21. The coefficient 16 is shown as coupled to the ALU 3 and multiplexers 15 and 21. The accumulator data 14 is also shown connected to multiplexers 15 and 21. Either of multiplexers 15 and 21 may selectively pass accumulator data 14 or coefficient data 16. While Applicant believes that one of skill in the art would clearly recognize this connectivity, the replacement drawing sheet attached hereto emphasizes this connectivity via large, darkened nodes adjacent to the ALU 3.

Example connectivity in FIG. 2 and related claims is supported at various portions of the Specification, including the following discussion from pages 4-5:

The multiplexer 15 is connected to receive accumulator data 14 from the output of the accumulator 7, and coefficient data 16 (coeff) from a coefficient port of the processing element 1. The multiplexer 15 is arranged to selectively provide the accumulator data 14 or coefficient data 16 as the input data 13 to the storage element 11, under control of a control signal 17, which comes from or forms part of the broadcast instruction 10. *See* page 4:18-23.

The multiplexer 21 is also connected to receive accumulator data 14 from the accumulator 7, and coefficient data 16 (coeff) from the coefficient port of the processing element 1. *See page 4:25-27.*

In other words, the multiplexer 15 is controlled to pass the accumulator data 14 as the input data 13 to the storage element 11, while the multiplexer 21 is controlled to pass the coefficient data 16, which acts as the index 19 to the respective storage location SE_Y in which the data is to be stored. This enables the correct values to be stored in the correct locations SE_1 - SE_N in the storage element 11.

Alternatively, if desired, the coefficients can be stored by applying the coefficient data to the input 13 and using the accumulator data as the index 19. *See page 5:6-12.*

In connection with these example embodiments, the multiplexers 15 and 21 receive at least part of an instruction 10, accumulator data 14 and coefficient data 16, and are coupled to pass received data to a storage element.

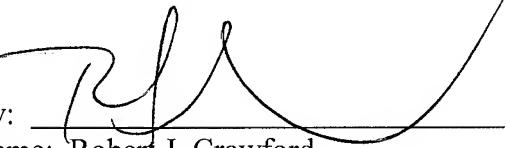
Further regarding the Section 112(2) rejection of claims 7 and 23 and the Examiner's suggestion (at page 5) that a "transformation" of data must be added to the claims, Applicant respectfully declines to add limitations. The limitations referenced by the Examiner are directed to a multiplexer that passes "accumulator data to the storage element when storing coefficient data." Applicant submits that no transformation is claimed or otherwise required. As is consistent with the example embodiments cited above, different types of data may be transferred during other operations, such that accumulator data may be passed during a storage operation in which coefficient data is stored. Here, the claims are directed to passing accumulator data during a storage operation with coefficient data. Applicant has amended claims 7 and 23 in a manner consistent with the specification and the above discussion.

In view of the remarks above, Applicant believes that each of the rejections/objections has been overcome and the application is in condition for allowance. Should there be any remaining issues that could be readily addressed over the telephone, the Examiner is asked to contact the agent overseeing the application file, Peter Zawilski, of NXP Corporation at (408) 474-9063.

Please direct all correspondence to:

Corporate Patent Counsel
NXP Intellectual Property & Standards
1109 McKay Drive; Mail Stop SJ41
San Jose, CA 95131

CUSTOMER NO. 65913

By: 
Name: Robert J. Crawford
Reg. No.: 32,122
(NXPS.469PA)